X-1462-2P US   PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Kevin T. Look et al.

Assignee: Xilinx, Inc.

Title: "Regulating Unused/Inactive Resources in
               Programmable Logic Devices for Static Power
               Reduction"

Serial No.:   10/783,589   File Date: 2/20/2004

Examiner: Unknown   Art Unit: 2171

Docket No.:   **X-1462-2P US**   Conf. No.: 1594

COMMISSIONER FOR PATENTS
Office of Initial Patent Examination's Filing Receipt Corrections
VIA FACSIMILE 703-746-9195

REQUEST FOR CORRECTED FILING RECEIPT

Dear Sir:

Applicant requests a corrected Filing Receipt in the above
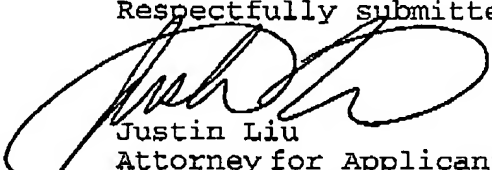identified patent application, which includes the following
changes:

Please correct the Title: **from** -- "Regulating
Unused/~~Interactive~~ Resources in Programmable Logic Devices for
Static Power Reduction" -- **to** "Regulating Unused/Inactive
Resources in Programmable Logic Devices for Static Power
Reduction".

A red-lined copy of the Filing Receipt is enclosed for
clarification.

No fee is due with this communication.

The Commissioner is hereby authorized to charge any
additional fees, or credit any overpayment which may be required
to Deposit Account No. 24-0040.

                               Respectfully submitted,

                               Justin Liu
                               Attorney for Applicants
                               Reg. No. 51,959
                               408-879-4641

*I hereby certify that this correspondence is being deposited with the United States
Postal Service as first class mail in an envelope addressed to: Commissioner for Patents,
VIA FACSIMILE, 703-746-9195 on June 10, 2004.*

Julie Matthews
Name   Signature

MAY 1 8 2004

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APPL. NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/783,589	02/20/2004	2171	770	X-1462-2P US	5	20	2

24309
XILINX, INC
ATTN: LEGAL DEPARTMENT
2100 LOGIC DR
SAN JOSE, CA 95124

CONFIRMATION NO. 1594

FILING RECEIPT

OC000000012659752

Date Mailed: 05/13/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

Xilinx, Inc., San Jose, CA;

Domestic Priority data as claimed by applicant

This application is a CIP of 10/666,669 09/19/2003

Foreign Applications

If Required, Foreign Filing License Granted: 05/13/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

RECEIVED
MAY 1 8 2004
INTELL. PROP. DEPT.
XILINX, INC.

Title

Regulating unused/interactive resources in programmable logic devices for static power reduction

inactive

Preliminary Class

707

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Title 37, Code of Federal Regulations, 5.11 & 5.15

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